



TRANSMITTAL FORM

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TRANSMITTAL FORM <i>(to be used for all correspondence during pendency of filed application)</i>	Application Number	10/606,419	
	Filing Date	June 26, 2003	
	First Named Inventor	Vigyan Singhal	
	Group Art Unit Number	2825	
	Examiner Name	Tuyen P. To	
Total Number of Pages in This Submission	22	Attorney Docket Number	24006-08742

ENCLOSURES (*check all that apply*)

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[16] Sheet(s) of Figure(s) [1-16]
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REMARKS:

SIGNATURE OF ATTORNEY OR AGENT

Signature: 
Attorney/Reg. No.: John T. McNelis, Reg. No. 37,186 Dated: 10 April 2006

CERTIFICATE OF MAILING

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IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE



APPLICANT(S):

Vigyan Singhal *et al.*

APPLICATION NO.:

10/606,419

FILING DATE:

June 26, 2003

TITLE:

System and Method for Guiding and Optimizing Formal Verification
for a Circuit Design

EXAMINER:

Tuyen P. To

GROUP ART UNIT:

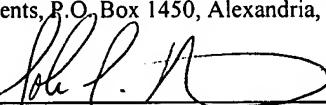
2825

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24006-08742

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LETTER TO THE CHIEF DRAFTSPERSON

SIR:

In response to the drawing informalities indicated on the Examiner's Notice Regarding Patent Drawings for the above-entitled patent application, please substitute the enclosed 16 (sixteen) sheets of formal drawings containing Figures 1-16 for the 16 (sixteen) sheets of drawings containing Figures 1-16 previously submitted.

Respectfully submitted,
 Vigyan Singhal

Dated: 10 April 2006By: 

John T. McNelis, Reg. No: 37,186

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